



# BC849B

NPN general purpose transistor

25 April 2023

Product data sheet

## 1. General description

NPN transistor in a small SOT23 Surface-Mounted Device (SMD) plastic package.

PNP complement: BC859B

## 2. Features and benefits

- Low current (max. 100 mA)
- Low voltage (max. 30 V)
- AEC-Q101 qualified

## 3. Applications

- General purpose switching and amplification

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{CE0}$	collector-emitter voltage	open base	-	-	30	V
$I_C$	collector current		-	-	100	mA
$h_{FE}$	DC current gain	$V_{CE} = 5\text{ V}; I_C = 10\ \mu\text{A}; T_j = 25\text{ }^\circ\text{C}$	-	240	-	

## 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	B	base	<p>SOT23</p>	<p>sym123</p>
2	E	emitter		
3	C	collector		

## 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
<a href="#">BC849B</a>	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	<a href="#">SOT23</a>

## 7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BC849B	2B%

[1] % = placeholder for manufacturing site code

## 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	30	V
$V_{CEO}$	collector-emitter voltage	open base		-	30	V
$V_{EBO}$	emitter-base voltage	open collector		-	5	V
$I_C$	collector current			-	100	mA
$I_{CM}$	peak collector current			-	200	mA
$I_{BM}$	peak base current			-	200	mA
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[1]	-	250	mW
$T_j$	junction temperature			-	150	°C
$T_{amb}$	ambient temperature			-65	150	°C
$T_{stg}$	storage temperature			-65	150	°C

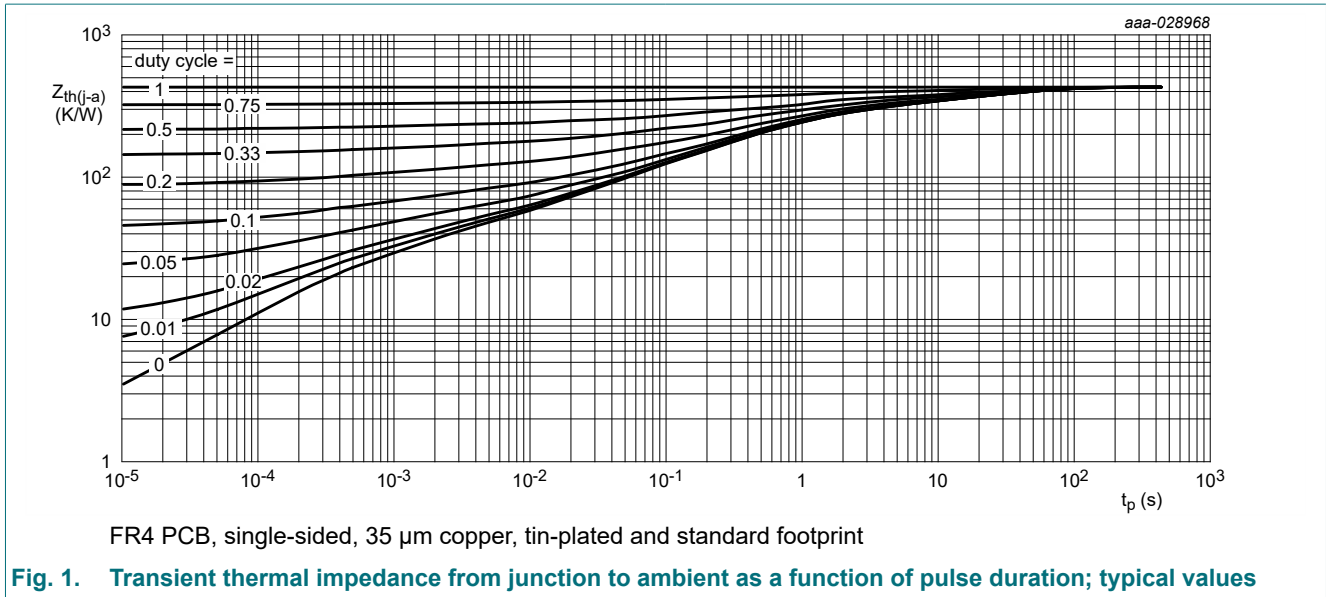
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient		[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



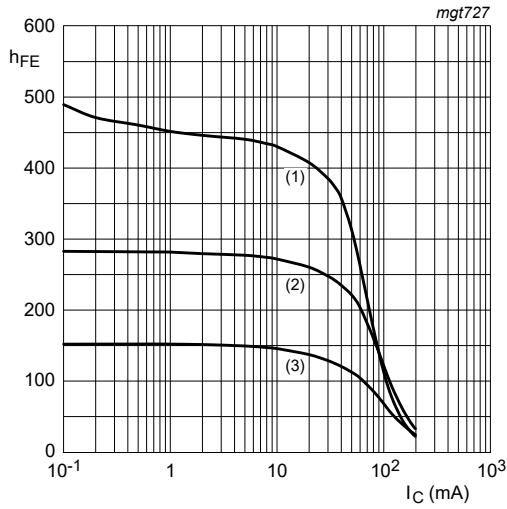
## 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
$I_{\text{CBO}}$	collector-base cut-off current	$V_{\text{CB}} = 30 \text{ V}; I_{\text{E}} = 0 \text{ A}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	-	15	nA	
		$V_{\text{CB}} = 30 \text{ V}; I_{\text{E}} = 0 \text{ A}; T_{\text{j}} = 150 \text{ }^{\circ}\text{C}$	-	-	5	$\mu\text{A}$	
$I_{\text{EBO}}$	emitter-base cut-off current	$V_{\text{EB}} = 5 \text{ V}; I_{\text{C}} = 0 \text{ A}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	-	100	nA	
$h_{\text{FE}}$	DC current gain	$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 10 \text{ } \mu\text{A}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	240	-		
		$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 2 \text{ mA}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	200	290	450		
$V_{\text{CEsat}}$	collector-emitter saturation voltage	$I_{\text{C}} = 10 \text{ mA}; I_{\text{B}} = 0.5 \text{ mA}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	90	250	mV	
		$I_{\text{C}} = 100 \text{ mA}; I_{\text{B}} = 5 \text{ mA}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	200	600	mV	
$V_{\text{BEsat}}$	base-emitter saturation voltage	$I_{\text{C}} = 10 \text{ mA}; I_{\text{B}} = 0.5 \text{ mA}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	[1]	700	-	mV	
		$I_{\text{C}} = 100 \text{ mA}; I_{\text{B}} = 5 \text{ mA}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	[1]	900	-	mV	
$V_{\text{BE}}$	base-emitter voltage	$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 2 \text{ mA}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	[2]	580	660	700	mV
		$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 10 \text{ mA}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	[2]	-	-	770	mV
$C_{\text{c}}$	collector capacitance	$V_{\text{CB}} = 10 \text{ V}; I_{\text{E}} = 0 \text{ A}; i_{\text{e}} = 0 \text{ A}; f = 1 \text{ MHz}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	2.5	-	pF	
$C_{\text{e}}$	emitter capacitance	$V_{\text{EB}} = 500 \text{ mV}; I_{\text{C}} = 0 \text{ A}; i_{\text{c}} = 0 \text{ A}; f = 1 \text{ MHz}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	11	-	pF	
$f_{\text{T}}$	transition frequency	$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 10 \text{ mA}; f = 100 \text{ MHz}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	100	-	-	MHz	
NF	noise figure	$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 200 \text{ } \mu\text{A}; R_{\text{S}} = 2 \text{ k}\Omega; B = 200 \text{ Hz}; f = 10 \text{ Hz to } 15.7 \text{ kHz}; T_{\text{j}} = 25 \text{ }^{\circ}\text{C}$	-	-	4	dB	
		$V_{\text{CE}} = 5 \text{ V}; I_{\text{C}} = 200 \text{ } \mu\text{A}; R_{\text{S}} = 2 \text{ k}\Omega; f = 1 \text{ kHz}; B = 200 \text{ Hz}$	-	-	4	dB	

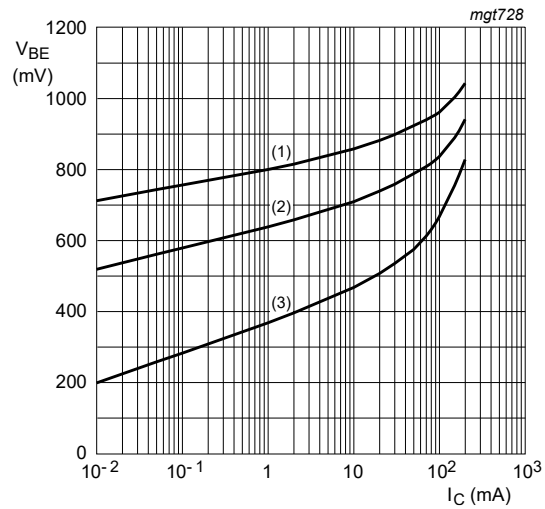
[1]  $V_{\text{BEsat}}$  decreases by about 1.7 mV/K with increasing temperature.

[2]  $V_{\text{BE}}$  decreases by about 2 mV/K with increasing temperature.



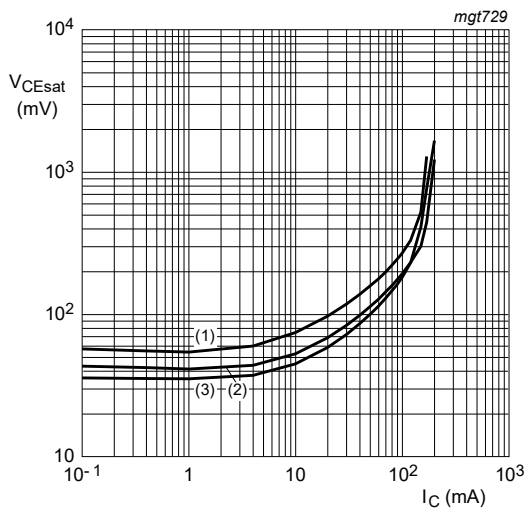
$V_{CE} = 5\text{ V}$   
 (1)  $T_{amb} = 150\text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

**Fig. 2. DC current gain as a function of collector current; typical values**



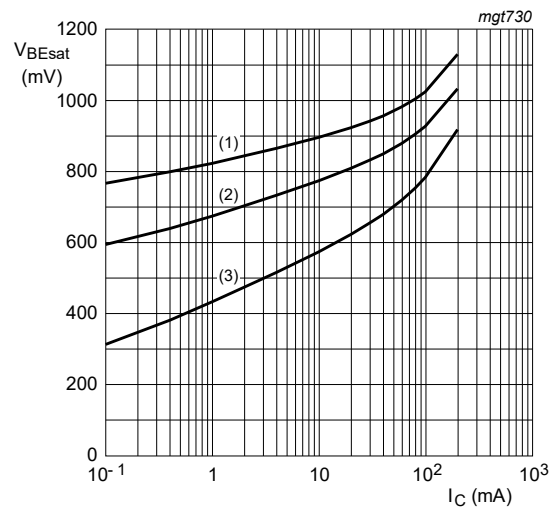
$V_{CE} = 5\text{ V}$   
 (1)  $T_{amb} = -55\text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = 150\text{ }^{\circ}\text{C}$

**Fig. 3. Base-emitter voltage as a function of collector current; typical values**



$I_C/I_B = 20$   
 (1)  $T_{amb} = 150\text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = -55\text{ }^{\circ}\text{C}$

**Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values**



$I_C/I_B = 10$   
 (1)  $T_{amb} = -55\text{ }^{\circ}\text{C}$   
 (2)  $T_{amb} = 25\text{ }^{\circ}\text{C}$   
 (3)  $T_{amb} = 150\text{ }^{\circ}\text{C}$

**Fig. 5. Base-emitter saturation voltage as a function of collector current; typical values**

## 11. Test information

### Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 12. Package outline

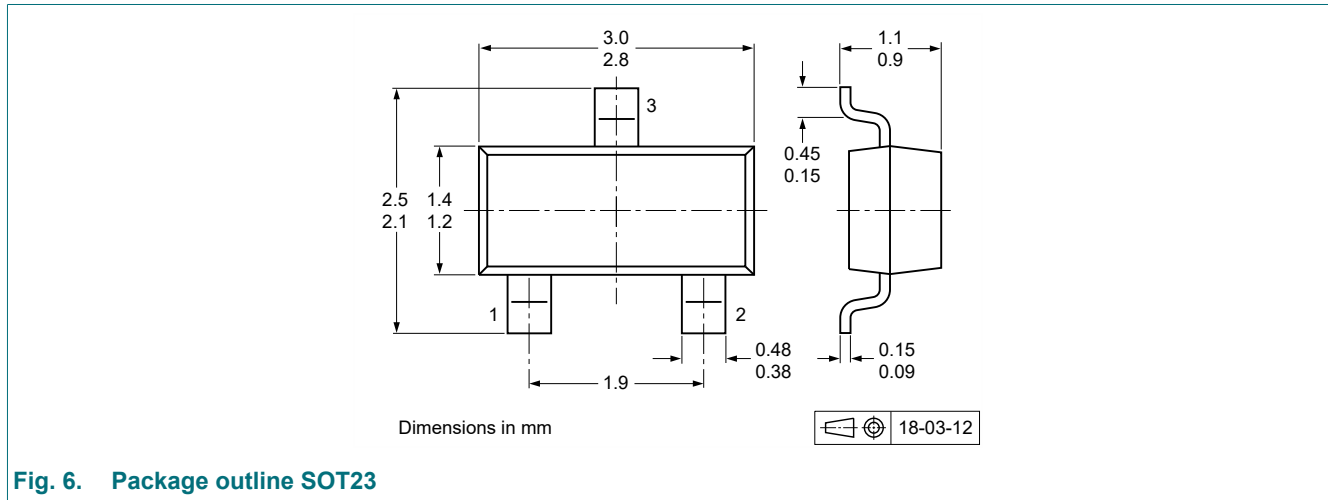


Fig. 6. Package outline SOT23

## 13. Soldering

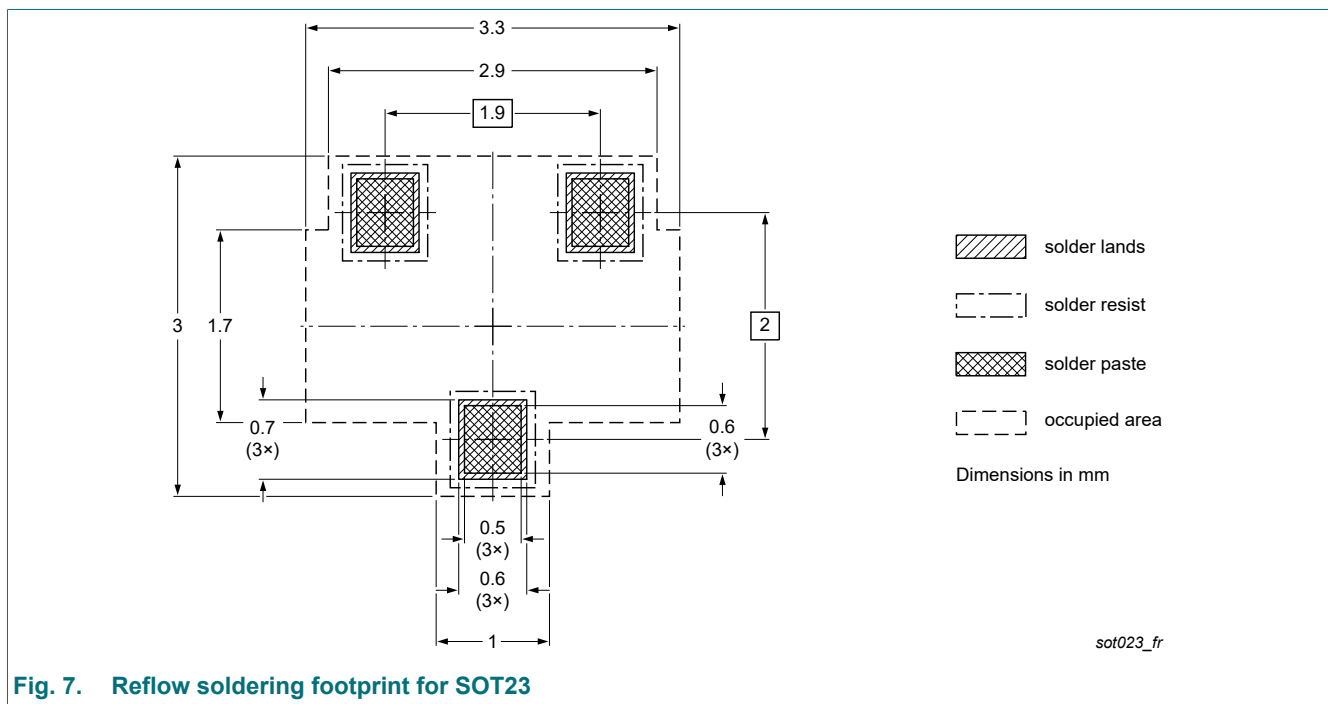


Fig. 7. Reflow soldering footprint for SOT23

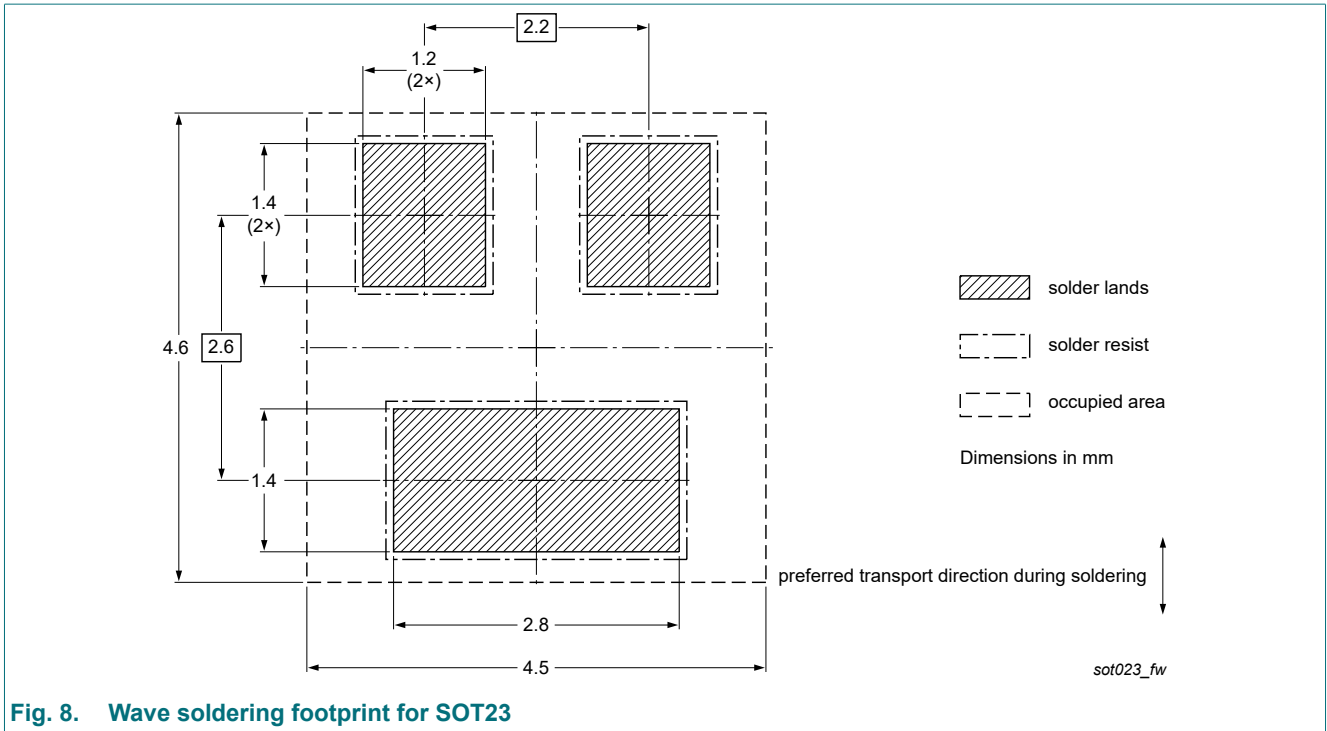


Fig. 8. Wave soldering footprint for SOT23

## 14. Revision history

**Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC849B v.3	20230425	Product data sheet	-	BC849_BC850 v.2
Modifications:	<ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li><li>• Family data sheet splitted to single type data sheets.</li></ul>			
BC849_BC850 v.2	20040116	Product data sheet	-	BC849_BC850 v.1
BC849_BC850 v.1	19990408	Product data sheet	-	-

## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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